

ABSTRACT OF THE DISCLOSURE

[0058] Method and apparatus for monitoring event occurrences, e.g., from an event signal, where a register and a counter are employed. In one embodiment, the register is designed to have a capture bit for capturing the occurrence of a monitored event. The shifting of the stored information within the capture bit to other bit locations within the register is controlled by a shift rate signal operating at a particular interval time period. At the expiration of the interval time period, the stored information in the capture bit is shifted within the register, where the capture bit is now free to detect the next occurrence of the monitored event. Since the register has a finite number of bit locations, as the captured information exists and/or enters the register, a counter is triggered to record the number of occurrences of monitored events. In this fashion, the counter is tracking the number of intervals in which the monitored events have occurred, whereas the register is displaying the most recent information as to which time intervals that the event occurred.